

Art Unit

Invention:

09/761,049

HIGH SPEED RECEIVER WITH INTEGRATED CMOS AND PECL CAPABILITY

1/16/2001

Address to:

**Assistant Commissioner for Patents** Washington, D.C. 20231

Transmitted herewith are:

2 sheets of formal drawing(s) for this application.

Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c) on the reverse side of the drawing.

Sean F. Sullivan Reg. No. 38,328

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**Customer Service #23413** 

Dated: March 12, 2001

I certify that this document and attached formal drawings are being deposited on 3/12/2001 U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Signature of Person Mailing Correspondence

Jessica L. Walsh

Typed or Printed Name of Person Mailing Correspondence